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equalizer transmitter adaptive ffe

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### [Adaptive equalizers and methods for carrying out equalization with ...](#)

An **adaptive equalizer** in a communication system having a precoder using **transmitter** coefficients for pre-equalizing the channel for post-cursor intersymbol ...

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### [Method and system for training adaptive channel equalization - US ...](#)

The receiver has an **adaptive linear equalizer (FFE)**, while the **transmitter** has a feedback **equalizer** (of the TML type). During the training period, ...

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### [Feed forward equalizer invention](#)

More particularly, the **adaptive equalizer** may comprise a least mean square (LMS) **adaptive** tapped delay-line **FFE**. An **FFE** may be utilized in the front-end of ...

[www.freshpatents.com/Feed-forward-equalizer-dt20060330ptan20060067542.php](http://www.freshpatents.com/Feed-forward-equalizer-dt20060330ptan20060067542.php) - 33k -

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### [Method and apparatus for implementing adaptive tomlinson-harashima ...](#)

It has an **adaptive linear equalizer (FFE)** in the receiver 3 and a feedback **equalizer (TML)** in the **transmitter** 1. During training, this system too operates ...

[www.freepatentsonline.com/20050129138.html](http://www.freepatentsonline.com/20050129138.html) - 36k - [Cached](#) - [Similar pages](#) - [Note this](#)

### [\[PDF\] Adaptive Equalizers Multipath Environment](#)

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16-QAM Channel **Equalizer Transmitter**. • Constellation and eye diagram ..... Coupling of **FFE** and **DFE**. **Adaptive Filters & Channel Equalizers** 46 ...

[www.sdrforum.org/pages/sdr03/FPGA%20Signal%20Processing/adaptive\\_short\\_1.pdf](http://www.sdrforum.org/pages/sdr03/FPGA%20Signal%20Processing/adaptive_short_1.pdf) - [Similar pages](#) - [Note this](#)

### [\[PDF\] A 10 Gb/s Adaptive Equalizer with Integrated Clock Data Recovery ...](#)

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**adaptive** circuitry, illustrated with a 2-tap, **FFE**, and [-tap] **DFE**. Fig. 5. **Equalizer** in measurements, ICSI, set-up, 3. Fabrication and packaging ...

[ieeexplore.ieee.org/iel5/9987/32092/01499756.pdf](http://ieeexplore.ieee.org/iel5/9987/32092/01499756.pdf) - [Similar pages](#) - [Note this](#)

### [\[PDF\] 26.7 - A BiCMOS 10Gb/s Adaptive Cable Equalizer - Solid-State ...](#)

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feedforward **equalizer (FFE)**, which gives variable high-fre- .... **Transmitter** in Standard 0.18 $\mu$ m CMOS," ISSCC Dig. Tech. Papers, pp. 248-249, Feb. 2002. ...

[ieeexplore.ieee.org/iel5/9264/29428/01332804.pdf](http://ieeexplore.ieee.org/iel5/9264/29428/01332804.pdf) - [Similar pages](#) - [Note this](#)

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### [\[PDF\] Microsoft PowerPoint - 05-342r0 SAS-2 Adaptive Equalizer Phy Layer ...](#)

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**Transmitter** De-emphasis and Receive Equalization (**FFE/DFE**) enhance the .... **Adaptive equalizer** are included in other Multi-Gbps standards (OIF and IEEE ). ...

[www.t10.org/ftp/t10/document.05/05-342r0.pdf](http://www.t10.org/ftp/t10/document.05/05-342r0.pdf) - [Similar pages](#) - [Note this](#)

**[PDF] Fiber Equalization**

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Analog Transversal Filter **Equalizer**. • Analog FFE/DFE implementation for ... equalization.

**Adaptive receiver, equalization, Adaptive, (transmitter) ...**

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**[PDF] Balanced equalization: The 10GBASE-KX4 formula for error-free ...**

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**Equalizer (FFE) in the transmitter** (with. feedbacks from the receiver), while sim- ... lution PE filter and an **adaptive receiver, equalizer** that can be ...

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### High stability fast tracking adaptive equalizer for use with time ...

The **adaptive equalizer** is also used to construct a RF receiver. The equalizer is constructed from a plurality of feed forward equalizer (**FFE**) sections and a ...

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### [PDF] A 10 Gb/s Adaptive Equalizer with Integrated Clock Data Recovery ...

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relax optical **transmitter** requirements. Vie equalizer presented here is ... trol (AGC), processed through a feed-forward equalizer (**FFE**) and applied ...

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### [PDF] A low-power, reconfigurable adaptive equalizer architecture ...

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lated to the distance between the **transmitter** and receiver ... forward equalizer (**FFE**), and the complex-valued feedback. equalizer (**FBE**). ...

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**Transmitter** De-emphasis and Receive Equalization (**FFE/DFE**) enhance the .... **Adaptive equalizer** are included in other Multi-Gbps standards (OIF and IEEE ). ...

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### Feed forward equalizer invention

More particularly, the **adaptive equalizer** may comprise a least mean square (LMS) adaptive tapped delay-line **FFE**. An **FFE** may be utilized in the front-end of ...

[www.freshpatents.com/Feed-forward-equalizer-dt20060330ptan20060067542.php](http://www.freshpatents.com/Feed-forward-equalizer-dt20060330ptan20060067542.php) - 33k -

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### Segmented equalizer invention

2 is shown to perform filter function by **FFE** filter 312 using sample inputs ... [0021] The performance requirement of an **adaptive equalizer** depends on the ...

[www.freshpatents.com/Segmented-equalizer-dt20070531ptan20070121717.php](http://www.freshpatents.com/Segmented-equalizer-dt20070531ptan20070121717.php) - 30k -

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### SIGNAL PROCESSING DEVICE CAPABLE OF ENHANCING CORRECTNESS OF ...

The device of claim 1 wherein the passband **adaptive equalizer** comprises an adder coupled respectively with the **FFE** and the **FBE** for outputting the equalized ...

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**High stability fast tracking adaptive equalizer for use with time ...**

The **adaptive equalizer** according to claim 1, wherein the tap coefficients of each **FFE** section are adapted in accordance with the cross correlation between ...

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Equalizer (**FFE**) in the **transmitter** (with. feedbacks from the receiver), while sim- .... been shown that an **adaptive equalizer** ...

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### FRONT END INTERFACE FOR DATA RECEIVER - Patent 20060159200

The DFE 13 operates as a deserializer as well as an **adaptive equalizer**. ... information for updating **FFE** coefficients of the **transmitter** from the receiver ...

[www.freepatentsonline.com/20060159200.html](http://www.freepatentsonline.com/20060159200.html) - 53k - Cached - Similar pages - Note this

### High speed data service via satellite modem termination system and ...

an **adaptive equalizer** coupled to the QAM demodulator for characterizing a RF ..... DC-Offset correction units 1416a and b correct any **DC voltage offset** at ...

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### Front end interface for data receiver invention

The prior art receiver complex 10 may be operated in conjunction with a **transmitter** having a feed forward equalizer (**FFE**), in which the tap coefficients ...

[www.freshpatents.com/Front-end-interface-for-data-receiver-dt20060720ptan20060159200.php](http://www.freshpatents.com/Front-end-interface-for-data-receiver-dt20060720ptan20060159200.php) - 34k - Cached - Similar pages - Note this

### EP1398891 Broadcom european software patent - High speed data ...

An **adaptive equalizer** & Ingress cancellation unit 618 characterizes the RF ..... DC-Offset correction units 1416a and b correct any **DC voltage offset** at the ...

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"adaptive equalizer" AND transmitter AND ffe AND "dc

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Or

1. **High speed data service via satellite modem termination system and satellite modems**

**Lin, Dorothy / Brescia, Rocco J. / Chien, Jen-chieh / Gin, Alan / Dale, Mark / Fanous, Adel F. (Broadcom Corporation ; Lin, Dorothy D. ; Chien, Jen-chieh ; Gin, Alan ; Brescia, Rocco J., Jr. ; Fanous, Adel F. ; Dale, Mark R.), EUROPEAN PATENT APPLICATION, Mar 2004**

patno:EP1398891

...that the CMTS provide a single carrier **transmitter** for each downstream (i.e. from head...channel is thus characterized by many **transmitters** (CMs) and one receiver (the CMTS). Time...includes not only a receiver but also a **transmitter**. While the newer satellite communication...

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2. **High speed data service via satellite modem termination system and satellite modems**

**Lin, Dorothy D. / Brescia, Rocco J. / Chien, Jen-Chieh / Fanous, Adel F. / Gin, Alan / Dale, Mark R., UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION, Jun 2004**

patno:US20040105403

...preamble for signal acquisition. An **adaptive equalizer** & Ingress cancellation unit...QAM demodulator 610 and the **adaptive equalizer** & Ingress cancellation unit...a turbo decoder by a 12-tap **FFE** unit 716. The FEC decoder includes...

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1. [Adaptive equalizer](#)  
**Upton, Eric L. (NORTHROP GRUMMAN CORPORATION), EUROPEAN PATENT APPLICATION, Nov 2004**  
patno:EP1475934  
...equalizer employing a feed forward equalizer (**FFE**) processor, a decision feedback equalizer...weight values applied to weight taps in the **FFE** processor and the DFE processor. (2. Discussion..:cable several thousand miles between a **transmitter** and a receiver, the distortion may be...sometimes called a feed forward equalizer (**FFE**) processor, and an infinite impulse response...  
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2. [TIMING RECOVERY FOR A HIGH SPEED DIGITAL DATA COMMUNICATION SYSTEM BASED ON ADAPTIVE EQUALIZER IMPULSE RESPONSE CHARACTERISTICS](#)  
**JONSSON, Ragnar / OLAFSSON, Sverrir / BJARNASON, Elias (Conexant Systems, Inc.), EUROPEAN PATENT, Jul 2001**  
patno:EP1118183  
...associated with the **transmitter**. In systems having...associated with an **adaptive equalizer** structure employed...structure 108 is an **adaptive equalizer** structure having...feedforward equalizer (**FFE**), a decision feedback...transmitted by a **transmitter** located at the...configured as an **adaptive equalizer** structure. Echo...  
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3. [High stability fast tracking adaptive equalizer for use with time varying communication channels](#)  
**Sommer, Naftali / Shalvi, Ofir / Segal, Mordechai (Texas Instruments Incorporated), UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Apr 2002**  
patno:US6366613  
...DTV Digital Television **FFE** Feed Forward Equalizer...equalization, either **FFE** or DFE, in the receiver...then equalized by the **adaptive equalizer** unit 16 that functions...and colored noise. The **adaptive equalizer** unit 16 also functions...

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4. High stability fast tracking adaptive equalizer for use with time varying communication channels

**Sommer, Naftali / Shalvi, Ofir / Segal, Mordechai, UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION, Nov 2001**  
patno:US20010043650

...equalization, either **FFE** or DFE, in the receiver...then equalized by the **adaptive equalizer** unit 16 that functions...and colored noise. The **adaptive equalizer** unit 16 also functions...feedforward equalizer (**FFE**) sections 32 and a plurality...

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5. High stability fast tracking adaptive equalizer for use with time varying communication channels

**Sommer, Naftali / Shalvi, Ofir / Segal, Mordechai (Texas Instruments Incorporated), UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, May 2001**  
patno:US6240133

...DTV Digital Television **FFE** Feed Forward Equalizer...equalization, either **FFE** or DFE, in the receiver...then equalized by the **adaptive equalizer** unit 16 that functions...and colored noise. The **adaptive equalizer** unit 16 also functions...

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6. TIMING RECOVERY FOR A HIGH SPEED DIGITAL DATA COMMUNICATION SYSTEM BASED ON ADAPTIVE EQUALIZER IMPULSE RESPONSE CHARACTERISTICS

**JONSSON, Ragnar / OLAFSSON, Sverrir / BJARNASON, Elias (CONEXANT SYSTEMS, INC.), PATENT COOPERATION TREATY APPLICATION, Apr 2000**  
patno:WO0019655

...SYSTEM BASED ON **ADAPTIVE EQUALIZER** IMPULSE RESPONSE...associated with the **transmitter**. In systems...associated with an **adaptive equalizer** structure...transmitted by a **transmitter** located at...configured as an **adaptive equalizer** structure...

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7. Adaptive equalizer matched filter error metric concept and apparatus

**Upton, Eric L. (Northrop Grumman Corporation), UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION, Nov 2004**  
patno:US20040223544

...embodiment of the present invention, employing an **FFE** processor 12 and a DFE processor 14. The **FFE** processor 12 receives a distorted RF signal. In...from the processor 18. The operation of DFE and **FFE** processors for this purpose is well understood...

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8. Timing recovery for a high speed digital data communication system based on adaptive equalizer impulse response characteristics

**Jonsson, Ragnar / Olafsson, Sverrir / Bjarnason, Elias (Conexant Systems, Inc.), UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Jul 2002**  
patno:US6414990

...100 also includes an **adaptive equalizer** structure 108, which...structure 108 is an **adaptive equalizer** structure having adjustable...feedforward equalizer (**FFE**), a decision feedback...signal transmitted by a **transmitter** located at the same...be configured as an **adaptive equalizer** structure. Echo estimate...

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**9. Gain adaptive equalizer**

**Jones, Keith R. / Trevino, Gilberto Isaac Sada / Jones, William W., UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION**, Jan 2004  
patno:US20040005001

...diagram of one example embodiment of an **adaptive equalizer**. As shown an input 200 connects to...embodiment the equalizer 310 comprises an **adaptive equalizer**. In one embodiment the equalizer...and □ comprise tap values for an **adaptive equalizer** having two or more taps. The values...

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**10. ADAPTIVE EQUALIZERS AND METHODS FOR CARRYING OUT EQUALIZATION WITH A PRECODED TRANSMITTER**

**LING, Stanley K. / AN, Ping / TAKATORI, Hiroshi (LEVEL ONE COMMUNICATIONS, INC.), PATENT COOPERATION TREATY APPLICATION**, Sep 1998  
patno:WO9839871

...receiver 312 just the **FFE** 362 are needed, since the output of the **FFE** should be ISI free...coefficients at the **transmitter** are locked in and thus...be dealt with in the **FFE** by continuous adaptation...are now used in the **transmitter**, where the decision...there is a need for an **adaptive equalizer** for adapting a precoder...

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**11. Adaptive equalizers and methods for carrying out equalization with a precoded transmitter**

**Ling, Stanley K. / An, Ping / Takatori, Hiroshi (Level One Communications, Inc.), UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT**, Dec 2000  
patno:US6167082

...acceptable performance without updating the **transmitter** coefficients. If the **FFE** 810 alone can not cancel an acceptable...the Tomlinson precoder 324 at the **transmitter** 310, and at the receiver 312 just the **FFE** 362 are needed, since the output of the **FFE** should be ISI free. However, once...328 changes, the coefficients at the **transmitter** are locked in and thus can't be changed...

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**12. SINGLE CLOCK RECEIVER**

**GREISS, Israel / BUBLIL, Baruch / JACOB, Jeffrey / TAICH, Dimitry (MYSTICOM LTD.), PATENT COOPERATION TREATY APPLICATION**, Jun 2003  
patno:WO03053019

...includes 10 multiple analog **transmitters** and multiple analog receivers...phase-locked loop oscillator. The **transmitters** and receivers are coupled...for the 25 receivers and **transmitters**, there is a substantial...feed forward equalizer (**FFE**). Each **FFE** drives a "slicer...apparatus includes a forward **adaptive equalizer** that is implemented to remove...

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13. Method and apparatus for implementing a channel correction in a digital data link

**Laamanen, Heikki / Vaananen, Janne, UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION, Feb 2003**  
patno:US20030035495

...linear equalizer (**FFE**) and the DFE are adjusted...replacement of an **adaptive equalizer** by an entirely fixed...path, as well as a **transmitter** according to claim...the receiver, the **transmitter** or partially in both...the use of a linear **adaptive equalizer (FFE)**. However, a linear...

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14. Apparatus and method for digital data transmission

**Rakib, Selim Shlomo / Azenkot, Yehuda (Terayon Communication Systems, Inc.), EUROPEAN PATENT APPLICATION, Sep 2001**  
patno:EP1130918

...a physically distributed system of **transmitters**. Of course all nonbaseband CDMA systems...into the spreading circuitry of the **transmitter**, but also exactly in phase therewith...Further, in CDMA systems with multiple **transmitters** which are physically distributed each...

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15. Head end receiver for digital data delivery systems using mixed mode SCDMA and TDMA multiplexing

**Azenkot, Yehuda / Lind, Paul Alan / Grimwood, Michael / Rakib, Selim Shlomo (Terayon Communication Systems, Inc.), EUROPEAN PATENT APPLICATION, Aug 2002**  
patno:EP1235402

...interference and group delay. The cable modem **transmitters** in such a system do some signal processing...processing and multiplexing that the **transmitters** did. The signal processing function...This gap is used by the cable modem **transmitters** (hereafter the CMs) to perform DOCSIS...

**Full text available at patent office. For more in-depth searching go to**  **view all 115 results from Patent Offices**  
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16. Fast skew detector

**Weiss, Rami / Bublil, Baruch / Greiss, Israel, UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION, Jul 2003**  
patno:US20030142772

...31 (in the corresponding **transmitter** module 40) and VGA 25 (in...**equalizer** or as a feed forward **adaptive equalizer (FFE)**). A block 186 conceptually...described below. Operating as an **FFE** equalizer, paths "1" are...for the non-corresponding **transmitters** 40 of transceiver 20) and...

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17. Combined feed forward and blind equalizer

**Greiss, Israel / Bublil, Baruch / Jacob, Jeffrey / Taich, Dmitry, UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION, Jul 2003**  
patno:US20030138039

...31 (in the corresponding **transmitter** module 40) and VGA 25 (in...**equalizer** or as a feed forward **adaptive equalizer (FFE)**). A block 186 conceptually...described below. Operating as an **FFE** equalizer, paths "1" are...for the non-corresponding **transmitters** 40 of transceiver 20) and...

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**18. Echo and near end cross talk reduction**

**Greiss, Israel / Bublil, Baruch / Jacob, Jeffrey / Taich, Dimitry, UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION, Jul 2003**  
patno:US20030138038

...31 (in the corresponding **transmitter** module 40) and VGA 25 (in...**equalizer** or as a feed forward **adaptive equalizer (FFE)**). A block 186 conceptually...described below. Operating as an **FFE** equalizer, paths "1" are...for the non-corresponding **transmitters** 40 of transceiver 20) and...

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**19. Frequency and timing recovery**

**Greiss, Israel / Bublil, Baruch / Jacob, Jeffrey / Taich, Dimitry, UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION, Sep 2003**  
patno:US20030182619

...31 (in the corresponding **transmitter** module 40) and VGA 25 (in...**equalizer** or as a feed forward **adaptive equalizer (FFE)**). A block 186 conceptually...described below. Operating as an **FFE** equalizer, paths "1" are...for the non-corresponding **transmitters** 40 of transceiver 20) and...

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**20. Apparatus and method for digital data transmission**

**Rakib, Selim Shlomo / Azenkot, Yehuda (Terayon Communication Systems, Inc.), EUROPEAN PATENT APPLICATION, Sep 2001**  
patno:EP1130919

...a physically distributed system of **transmitters**. Of course all nonbaseband COMA systems...into the spreading circuitry of the **transmitter**, but also exactly in phase therewith...Further, in CDMA systems with multiple **transmitters** which are physically distributed each...

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Volume 41, Issue 12, Dec. 2006 Page(s):2885 - 2900  
Digital Object Identifier 10.1109/JSSC.2006.884342  
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Beukema, T.; Sorna, M.; Selander, K.; Zier, S.; Ji, B.L.; Murfet, P.; Mason, J.; F Ainspan, H.; Parker, B.; Beakes, M.; [Solid-State Circuits, IEEE Journal of](#)  
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Digital Object Identifier 10.1109/JSSC.2005.856584  
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- 3. A 6.25-Gb/s binary transceiver in 0.13-/spl mu/m CMOS for serial data tra across high loss legacy backplane channels  
Payne, R.; Landman, P.; Bhakta, B.; Ramaswamy, S.; Song Wu; Powers, J.D.; Yee, A.-L.; Gu, R.; Lin Wu; Yiqun Xie; Parthasarathy, B.; Brouse, K.; Mohamm K.; Gupta, V.; Dyson, L.; Wai Lee; [Solid-State Circuits, IEEE Journal of](#)  
Volume 40, Issue 12, Dec. 2005 Page(s):2646 - 2657  
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1. **A 6.4-Gb/s CMOS SerDes core with feed-forward and decision-feedback equalizer**  
Beukema, T.; Sorna, M.; Selander, K.; Zier, S.; Ji, B.L.; Murfet, P.; Mason, J.; Fainman, H.; Parker, B.; Beakes, M.;  
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2. **A 6.4Gb/s CMOS SerDes core with feedforward and decision-feedback equalizer**  
Sorna, M.; Beukerna, T.; Selander, K.; Zier, S.; Ji, B.; Murfet, P.; Mason, J.; Rofeh, H.; Parker, B.;  
*Solid-State Circuits Conference, 2005. Digest of Technical Papers. ISSCC. 2005 International*  
6-10 Feb. 2005 Page(s):62 - 585 Vol. 1  
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*Solid-State Circuits, IEEE Journal of*  
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2. **10+ gb/s 90-nm CMOS serial link demo in CBGA package**  
Rylov, S.; Reynolds, S.; Storaska, D.; Floyd, B.; Kapur, M.; Zwick, T.; Gowda, T.;  
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3. **40-Gb/s circuits built from a 120-GHz f/sub T/ SiGe technology**  
Freeman, G.; Meghelli, M.; Kwark, Y.; Zier, S.; Rylakov, A.; Sorna, M.A.; Tanji, O.M.; Walter, K.; Jae-Sung Rieh; Jagannathan, B.; Joseph, A.; Subbanna, S.;  
*Solid-State Circuits, IEEE Journal of*  
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Digital Object Identifier 10.1109/JSSC.2002.801170  
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4. **A 0.18-/spl mu/m SiGe BiCMOS receiver and transmitter chipset for SONET transmission systems**  
Meghelli, M.; Rylakov, A.V.; Zier, S.J.; Sorna, M.; Friedman, D.;  
*Solid-State Circuits, IEEE Journal of*  
Volume 38, Issue 12, Dec 2003 Page(s):2147 - 2154  
Digital Object Identifier 10.1109/JSSC.2003.818571  
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"variable gain amplifier" "peaking amplifier"

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### Variable gain amplifier - Patent # 7250814 - PatentGenius

A method of varying the gain of an amplifier and an amplifier array are provided. The amplifier array includes two or more amplifier stages (201, ...

[www.patentgenius.com/patent/7250814.html](http://www.patentgenius.com/patent/7250814.html) - 51k - [Cached](#) - [Similar pages](#) - [Note this](#)

### Patents in Class 330/310

The **peaking amplifier** path includes a peaking a... 147, US6259321, CMOS **variable gain amplifier** and control method therefor A CMOS high frequency **variable** ...

[www.freepatentsonline.com/CCL-330-310-p3.html](http://www.freepatentsonline.com/CCL-330-310-p3.html) - 62k -

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### Patents in Class 330/124R

A digitally-controlled **variable-gain amplifier** is disclosed, which comprises a ... formats includes a carrier amplifier (30) and a **peaking amplifier** (40). ...

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### High gain, high efficiency power amplifier invention

Ideally, this would result in a **peaking amplifier** 14 with 6 dB more gain than that achieved ... **Variable gain amplifier** and **variable gain amplifier** module ...

[www.freshpatents.com/High-gain-high-efficiency-power-amplifier-dt20070607ptan20070126502.php](http://www.freshpatents.com/High-gain-high-efficiency-power-amplifier-dt20070607ptan20070126502.php) - 32k - [Cached](#) - [Similar pages](#) - [Note this](#)

### High speed multi-mode receiver invention

In a second mode, the **variable gain amplifier** is operable to amplify the signal with fixed gain and the **peaking amplifier** is operable to amplify the signal ...

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The receiver features a **variable-gain amplifier** (VGA), DFE, and ... The VGA drives a second-stage **peaking amplifier** that is used to ...

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### Peaking control for wideband laser driver applications - US Patent ...

**Variable gain amplifier** Issued on: December 19, 2000 ... comprises a laser modulator amplifier and said second amplifier comprises a **peaking amplifier**. ...

[www.patentstorm.us/patents/6750717-claims.html](http://www.patentstorm.us/patents/6750717-claims.html) - 22k - [Cached](#) - [Similar pages](#) - [Note this](#)

### CAT.INIST

The receiver features a **variable-gain amplifier** (VGA) with gain ranging from -6 to +10 dB in ~1 dB steps, an analog **peaking amplifier**, and a continuously ...

[cat.inist.fr/?aModele=afficheN&cpsidt=17306518](http://cat.inist.fr/?aModele=afficheN&cpsidt=17306518) - [Similar pages](#) - [Note this](#)

### High efficiency amplifier

The output of the **peaking amplifier** electrically acts like a current source and ... Under a low input signal level drive condition, the **peaking amplifier** is ...  
[patentplex.com/automatic\\_amplifier\\_disabling](http://patentplex.com/automatic_amplifier_disabling)  
[switch\\_ci330\\_sc51/high\\_efficiency\\_amplifier\\_6922102.html](http://switch_ci330_sc51/high_efficiency_amplifier_6922102.html) - 153k -  
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[PDF] [A Novel Envelope Following Power Amplifier with Power Tracking ...](#)

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**peaking amplifier** is adjusted to follow the dynamic envelope of CDMA signal ..... of peaking PA's gate bias, we designed a base band **variable gain amplifier** ...

[www.samsung.com/.../SocialCommitment/](http://www.samsung.com/.../SocialCommitment/)

[HumantechThesis/WinningPapers/downloads/work10/h21.pdf](http://HumantechThesis/WinningPapers/downloads/work10/h21.pdf) - [Similar pages](#) - Note this

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### High speed multi-mode receiver invention

[0003] A **DFE** is used at a receiver to counter the effects of distortion present in a .... unit including a **variable gain amplifier** and a **peaking amplifier** ...

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The receiver features a **variable-gain amplifier** (VGA) with gain ranging from -6 ... an analog **peaking amplifier**, and a continuously adapted **DFE**-based data ...  
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### High Speed Multi-Mode Receiver - Patent 20060067440

In a first mode, the **variable gain amplifier** is operable to amplify a ..... In one embodiment, in **DFE** mode, the **peaking amplifier** 320 operates with constant ...

[www.freepatentsonline.com/20060067440.html](http://www.freepatentsonline.com/20060067440.html) - 67k - Cached - Similar pages - Note this

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**peaking amplifier** followed by a 5-tap **DFE**, enables operation on ISI channels with over 30dB loss at ..... **Variable-Gain Amplifier**, Video Graphics Array ...

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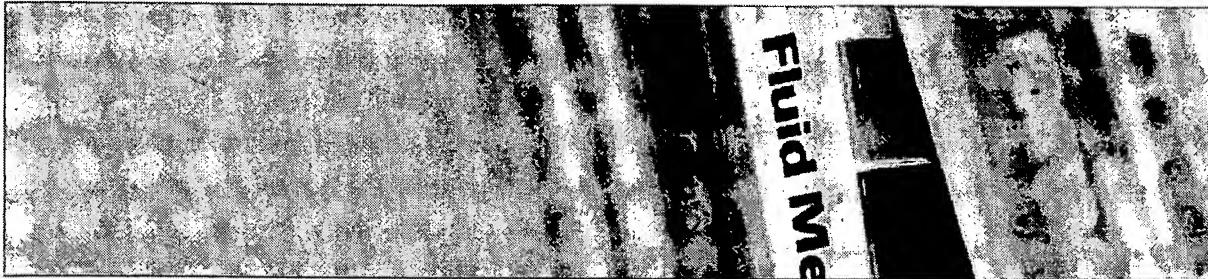
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A 6.4-Gb/s CMOS serdes core with feed-forward and decision-feedback equalization

**Auteur(s) / Author(s)**

BEUKEMA Troy<sup>(1)</sup>; SOMA Michael<sup>(2)</sup>; SELANDER Karl<sup>(2)</sup>; ZIER Steven<sup>(2)</sup>; JI Brian L.<sup>(2)</sup>; MURFET Phil<sup>(3)</sup>; MASON James<sup>(3)</sup>; RHEE Woogeun<sup>(1)</sup>; AINSPAN Herschel<sup>(1)</sup>; PARKER Benjamin<sup>(1)</sup>; BEAKES Michael<sup>(1)</sup>;

**Affiliation(s) du ou des auteurs / Author(s) Affiliation(s)**

(1) IBM T. J. Watson Research Center, Yorktown Heights, NY 10598, ETATS-UNIS

(2) IBM Microelectronics, East Fishkill, NY 12533, ETATS-UNIS

(3) IBM U.K., Hursley, Winchester SO21 2JN, ROYAUME-UNI

**Résumé / Abstract**

A 4.9-6.4-Gb/s two-level SerDes ASIC I/O core employing a four-tap feed-forward equalizer (FFE) in the transmitter and a five-tap decision-feedback equalizer (DFE) in the receiver has been designed in 0.13- $\mu$ m CMOS. The transmitter features a total jitter (TJ) of 35 ps p-p at 10[-12] bit error rate (BER) and can output up to 1200 mVppd into a 100- $\Omega$  differential load. Low jitter is achieved through the use of an LC-tank-based VCO/PLL system that achieves a typical random jitter of 0.6 ps over a phase noise integration range from 6 MHz to 3.2 GHz. The receiver features a variable-gain amplifier (VGA) with gain ranging from -6 to +10 dB in ~1 dB steps, an analog peaking amplifier, and a continuously adapted DFE-based data slicer that uses a hybrid speculative/dynamic feedback architecture optimized for high-speed operation. The receiver system is designed to operate with a signal level ranging from 50 to 1200 mVppd. Error-free operation of the system has been demonstrated on lossy transmission line channels with over 32-dB loss at the Nyquist (1/2 Bd rate) frequency. The Tx/Rx pair with amortized PLL power consumes 290 mW of power from a 1.2-V supply while driving 600 mVppd and uses a die area of 0.79 mm<sup>2</sup>[2].

**Revue / Journal Title**

IEEE journal of solid-state circuits (IEEE j. solid-state circuits) ISSN 0018-9200 CODEN IJSCBC

**Source / Source**

2005, vol. 40, n°12, pp. 2633-2645 [13 page(s) (article)] (15 ref.)

**Langue / Language**

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Institute of Electrical and Electronics Engineers, New York, NY, ETATS-UNIS (1966) (Revue)

**Mots-clés anglais / English Keywords**

Integrated circuit ; Die ; Transmission loss ; Lossy line ; Transmission line ; System design ; Optimization ; Feedback regulation ; Gain ; Variable gain amplifier ; Phase noise ; Phase locked loop ; Voltage controlled oscillator ; Bit error rate ; Jitter ; Receiver ; Transceiver ; Custom circuit ; Decision feedback equalizers ; Complementary MOS technology ;

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**1. Improvements in or relating to inspection and control systems**

**LAYCAK JOHN FRANCIS / UPHOFF RUSSEL LESLIE (JONES & LAUGHLIN STEEL CORP), UNITED KINGDOM PATENT APPLICATION, Dec 1964**

patno:GB0978573

...3 is passed through a **variable gain amplifier** to a diode clipping and...44 is then fed to the **variable gain amplifier** 40 to control its output...amplification are achieved 95 in **peaking amplifier** 416 whose output (wave...

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**2. Inspection and control system**

**LAYCAK JOHN F / UPHOFF RUSSEL L, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Feb 1962**

patno:US3020034

...3 is passed through a **variable gain amplifier** to a diode clipping and...44 is then fed to the **variable gain amplifier** 40 to control its output...amplification are achieved in **peaking amplifier** 46 whose output (wave...

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**3. Improvements in or relating to inspection systems**

**MCCREANOR RICHARD ALBIN / UPHOFF RUSSEL LESLIE / LAYCAK JOHN FRANCIS / LIBENSCHERK RUDOLF STEVEN (JONES & LAUGHLIN STEEL CORP), UNITED KINGDOM PATENT APPLICATION, Dec 1964**

patno:GB0978572

PATENT SPECIFICATION DRAWINGS ATTACHED 978,572 Inventors: RICHARD ALBIN McCREANOR, RUSSEL LESLIE TUPHOFF, JOHN FRANCIS LAYCAK, and RUDOLF STEVEN LIBENSCHERK. Date of Application and filing Complete Specification: Nov 17, 1961. No 41334/61. Complete Specification Published: Dec 23, 1964. © Crown

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ou  
ou  
pe  
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sct  
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F

4. Inspection and control system

**MCCREANOR RICHARD A / LAYCAK JOHN F / UPHOFF RUSSEL L / LIBENSCHERK RUDOLPH S, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Feb 1962**

patno:US3020033

...O in FIG. 3 is passed through a **variable gain amplifier** 122 to a clipper 124. Waveform...of circuit 126 is then fed to the **variable gain amplifier** 122 to control its output level...154 to a phase inverter 156 and **peaking amplifier** 158 only during the time duration...

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5. Electronic surface inspection system

**LAYCAK JOHN F, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Jan 1962**

patno:US3019346

...Included in this circuit is a **variable gain amplifier** 98 which is connected...102 is thus fed to the **variable gain amplifier** 93 to control its output...amplification is achieved in **peaking amplifier** 184 whose output is then...

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6. Improvements in or relating to an electronic surface inspection system

**LAYCAK JOHN FRANCIS (JOHNS & LAUGHLIN STEEL CORP), UNITED KINGDOM PATENT APPLICATION, Nov 1964**

patno:GB0974335

Index at acceptance: PATENT SPECIFICATION DRAWINGS ATTACHED Inventor: JOHN FRANCIS LAYCAK 974,335 Date of Application and filing Complete Specification Oct 23, 1961. H a ' No 37921/61. Complete Specification Published Nov 4, 1964. ©g) Crown Copyright 1964. -G 4 H(I X, 3 E, 5 D, 6 B, 6 D, WA 3, 7 B,

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7. Quantized pulse modulated nonsynchronous clipped speech multi-channel coded communication system

**Miller, Ralph H. / Dyer, William W. / Waterbury, John A. / Carlson, Wayland A. / Eastman, Richard O. (The United States of America as represented by the Secretary of the Navy), UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Jan 1978**

patno:US4070550

1. A quantized pulse-modulated nonsynchronous clipped speech multi-channel coded communication system comprising transmitter terminals and receiver terminals; said transmitter terminals comprising a plurality of channel units, a timing unit, and a ...

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fast

"variable gain amplifier" AND "peaking amplifier"

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"variable gain amplifier" AND "peaking amplifier" AND

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IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

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*Solid-State Circuits, IEEE Journal of*  
Volume 40, Issue 12, Dec. 2005 Page(s):2633 - 2645  
Digital Object Identifier 10.1109/JSSC.2005.856584

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IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

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Beukema, T.; Sorna, M.; Selander, K.; Zier, S.; Ji, B.L.; Murfet, P.; Mason, J.; Fainman, E.

[Solid-State Circuits, IEEE Journal of](#)

Volume 40, Issue 12, Dec. 2005 Page(s):2633 - 2645

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